Reliability of MPPT Converter in Different Operating Modes

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Abstract—The reliability plays an important role in power electronic systems by which the number of system failures, repair costs, guarantee and etc are estimated. In this paper first, a boost Maximum Power Point Tracker (MPPT) converter is simulated in Discontinues Conduction Mode (DCM) and Continuous Conduction Mode (CCM) under different output power ratings. For these simulations, all of the inherent parameters of MOSFET and diodes are applied according to the actual types. Then, reliability calculation is done based on MIL-HDBK-217 standard. Results have shown that the MPPT converter would have better performance from reliability point of view in CCM operating mode.

Index Terms—MPPT, solar cell, photovoltaic, reliability, life time.

I. INTRODUCTION

In a photovoltaic system, the PV panel represents the most percentage of the total cost of the system, the battery storage system second expensive block and other system components such as power electronic inverters and converters contribute only a small percentage of system cost. Due to the high cost of solar cells, it is necessary to operate the PV panel at maximum power point (MPP). The maximum power produced by a solar cell changes with solar radiation and temperature as shown in Fig. 1 and Fig. 2 [1].

In order to optimize the ratio between output power and cost of installation, photovoltaic systems are supposed to draw maximum power from the modules continuously, regardless of weather conditions or load voltage. Maximum power point trackers, commonly known as MPPT, are systems that operate PV modules so that maximum power can be achieved in all radiation and temperature conditions. Fig. 3 shows a PV system includes MPPT.

The DC/DC conversion process implies in turn an associated effect of impedance transformation, i.e., the input impedance shows a dependence on a number of parameters such as load resistance, duty cycle, etc. In this sense, converters are similar to transformers. When they are used as impedance adaptors, except that in converters the adaptation parameter is not the turns ratio between the secondary and primary ones, but the duty cycle D, which can be governed electronically, a fact that corresponds to MPPT. Different topology of DC-DC converters are used as MPPT. Comparisons between different topology of converters for

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MPPT operation are given in [2]-[6].

Buck and Boost are suitable topologies [7]. Boost topology is selected in this paper for investigation.

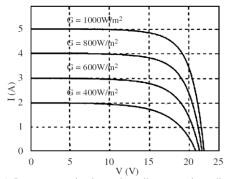


Fig. 1. Power generation in a solar cell versus solar radiation.

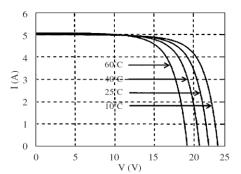


Fig. 2. Produced power in a solar cell versus temperature.

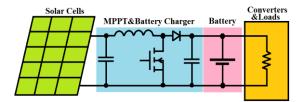


Fig. 3. A PV system including boost MPPT converter.

In switch mode converters the main filter inductor can operate in either continues or discontinues conduction mode. Performance comparison of CCM and DCM fly-back converters are given in [8]. Aspects being compared were component stress, output voltage regulation and transient response due to step load and efficiency.

The comparison was carried out experimentally on a 5V/25W, 50kHz prototype CCM and DCM fly-back converters which have been designed and built with similar circuit lay outs, components and power ratings. Tests performed on the prototype converter have shown that devices in the CCM fly-back converter sustain the same voltage stress, but less current stress than its DCM counterpart, when delivering the same output power.

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IADLE I: CONVERTERS PARAMETERS									
Output power	300 W		600) W	1000 W				
Operatin g mode	CCM	DCM	CCM	DCM	CCM	DCM			
MOSFET	<i>IXFH</i> 15N80Q	<i>IXFH</i> 15N80Q	<i>IXFH</i> 15N80Q	<i>IXFH</i> 15N80Q	APT8043 BFLL	APT8043 BFLL			
	$V_n = 1200 V$	$V_n = 1200V$	$V_n = 1200V$	$V_n = 1200V$	$V_n = 800V$	$V_n = 800V$			
	$I_n = 15 A$	$I_n = 15A$	$I_n = 15A$	$I_n = 15A$	$I_n = 20A$	$I_n = 20A$			
	$R_{Ds}(on) = 0.6 \Omega$	$R_{Ds}(on) = 0.6\Omega$	$R_{Ds}(on) = 0.6\Omega$	$R_{Ds}(on) = 0.6\Omega$	$R_{Da}(on) = 0.43\Omega$	$R_{Da}(on) = 0.43\Omega$			
	$\theta_{\rm jc} = 0.42^{\circ}C/W$	$\theta_{jc} = 0.42 ^{\circ}C/W$	$\theta_{jc} = 0.42 ^{\circ}C/W$	$\theta_{jc} = 0.42^{\circ}C/W$	$\theta_{jc} = 0.31^{\circ}C/W$	$\theta_{jc} = 0.31^{\circ}C/W$			
	DSIE12	DSIE12	DSIE12	DSIE12	DSIE12	DSIE12			
output	$V_n = 1200V$	$V_n = 1200V$	$V_n = 1200V$	$V_n = 1200V$	$V_n = 1200V$	$V_n = 1200V$			
Diode	$V_{f} = 2.6V$	$V_{f} = 2.6V$	$V_{f} = 2.6V$	$V_{f} = 2.6V$	$V_{f} = 2.6V$	$V_{f} = 2.6V$			
	$\theta_{jc} = 1.6^{\circ}C/W$	$\theta_{jc} = 1.6^{\circ}C/W$	$\theta_{jc} = 1.6^{\circ}C/W$	$\theta_{jc} = 1.6^{\circ}C/W$	$\theta_{jc} = 1.6^{\circ}C/W$	$\theta_{jc} = 1.6^{\circ}C/W$			
Input Bridge	KBPC3506	KBPC3506	KBPC3506	KBPC3506	KBPC3506	KBPC3506			
capacitor	$220 \mu F$	330 µF	470 μF	560 μF	$680 \mu F$	$2*470 \ \mu F$			
	450 V	450 V	450 V	450 V	450 V	450 V			
Inductor	1.4 <i>mH</i>	1.2 <i>mH</i>	700 mH	600 mH	400 mH	370 mH			
f_s	25 <i>kHz</i>	25 <i>kHz</i>	25 <i>kHz</i>	25 <i>kHz</i>	25 <i>kHz</i>	25 <i>kHz</i>			

TABLE I: CONVERTERS PARAMETERS

In this comparison, reliability is absent and is not considered, where as reliability is a key necessity in power electronic devices by which the life time, number of failures and associated cost are estimated. In [9], effect of leakage inductance on reduction of reliability of switching power supplies is discussed. In [10], the reliability of fuel cell DC-DC converters is calculated and optimum topology from reliability point of view is presented. In this paper, a boost converter is considered as the MPPT part of a photovoltaic system. Then the simulation and reliability calculation of boost converter in DCM and CCM modes operating in three different power ratings is done. Reliability calculation is based on MIL-HDBK217 standard and in part counting method [11].

II. DEFINITION OF RELIABILITY

Probability of proper function of a system after a time interval is referred to as its reliability, which is dependent on the type and quality of the parts and materials used in the device, tension each part endures, the ambient conditions in which the device is working, etc. The failure rate in most of electronic systems is constant and is represented by λ ; the reliability is expressed by:

$$R(t) = e^{-\lambda t} \tag{1}$$

The mathematical mean of R(t) occurs at:

$$t = \frac{1}{\lambda} \tag{2}$$

Which is the amount of time that should elapse until the first failure occurs. This is called the Mean Time to Failure (MTTF). The mean time to repair (MTTR) of the system is negligible compared to MTTF, so the mean time between failures (MTBF) of a system is expressed as:

$$MTBF = MTTR + MTTF = \frac{1}{\lambda}$$
(3)

The total rate of the system failure is the sum of the failure

rates of all parts of the system:

$$\lambda_{system} = \sum_{n=1}^{N} \lambda_{part} \tag{4}$$

Hence, the reliability of the system will be the product of all the system components' reliabilities [6]:

$$R_{system} = \prod R_{part} \tag{5}$$

III. SIMULATION RESULTS

To compare MPPT application of boost converter in DCM and CCM operating modes from reliability point of view, the boost converter shown in Fig. 1 is considered. The converter is simulated for three output power of 300W, 600W, 1000W and for two operating modes of CCM and DCM using ORCAD software. In order to equalize simulation results to actual results, all of the inherent parameters of MOSFET and diodes are applied according to the actual types. The simulation parameters for different output powers and operating modes are shown in Table I. It should be mentioned that to achieve overlap waveforms of voltage and current at turn-on/turn-off instants, maximum step time of simulation is considered as 1 ns. Simulation results of the converter in different output powers and CCM/DCM operating modes is shown in Table II. Voltage and current waveform of main switch and diode are shown in Fig. 4. It is noticeable that voltage and current are shown on a unit picture for overlap monitoring.

IV. RELIABILITY CALCULATIONS

In this section, reliability of the boost converter in 1000 W output power and CCM operating mode is calculated and presented in details. For different output powers and operating modes, results of reliability calculations are shown in Table III. The part counting method [11] is used to calculate reliability. In this approach, first the failure rate of

each element in the converter structure is obtained individually and then the value of the converter's MTBF is calculated from equation (4) that "N" is the number of consisting parts.

For these calculations the following assumptions are made:

- The ambient temperature is 27°C.
- The control structures of these converters are the same as the control circuit used in [9] whose reliability is also calculated in [9].

To calculate the reliability, first the dynamic and static losses of MOSFET and Diodes should be calculated for different output powers working in two operating modes of DCM and CCM. The dynamic loss of input bridge diodes can be neglected. But, the important point that should be noted here is that if the converter is operating in DCM mode, then before further turn-on of the switch, the inductor current is reached to zero. So, there will not be the turn-on loss. Vice versa, in CCM operating mode, since in turn-on instant for the switch, the current should be transferred from diode to the switch, then the dynamic loss includes both turn-on/turn-off losses.

The peak of turn-on/turn-off loss is calculated by equation (6).

$$P = V_{avg} \times I_{avg} \times t_{ol} \times f_s \tag{6}$$

where V_{avg} and I_{avg} are the average voltage across the switch and the average current follows through the switch during turn-on/turn-off overlap times respectively. t_{ol} is the turn-on/turn off overlap duration and f_s is the switching frequency of the circuit. Using equation (6) and considering Fig. 4, the peak of turn-on/turn-off losses of MOSFET during overlap time is calculated for 1000W output power in DCM and CCM operating modes as below:

In CCM operating mode:

$$P_{loss(Turn-off)} = \frac{400}{2} \times \frac{190}{25} \times \frac{794}{40000} = 30.17W$$

$$P_{loss(Turn-on)} = \frac{400}{2} \times \frac{150}{25} \times \frac{311}{40000} = 9.33W$$

In DCM operating mode:

$$P_{loss(Turn-off)} = \frac{400}{2} \times \frac{350}{25} \times \frac{780}{40000} = 54.6W$$

TABLE II. SIMULATION RESULTS FOR DIFFERENT OUTPUT POWERS IN DCM AND CCM OPERATING MODES

Output power		300W		600W		1000W
Operating mode	CCM	DCM	CCM	DCM	CCM	DCM
$P_{dynamic}(MOSFET)(W)$	7.5	15	15	25.5	25	45.5
$P_{static}(MOSFET)(W)$	0.486	1.26	1.94	3.75	3.9	9.5
P _{dynamic} (output Diode)(W)	0.75	0.75	1.25	1.25	2.35	1.8
P _{static} (output Diode)(W)	3	3.25	6	6.25	10	10.5
$P_{loss}(input Bridge)(W)$	3.26	4.1	6	7.25	8.4	12.6
Efficiency %	93.2	90.6	93.8	92	94.1	91.5

The values calculated above for the turn-on/turn-off losses are the peak of these losses (at 5 msec) that should be integrated in duration of the input voltage frequency (50Hz). This integration is done by the ORCAD software and the average dynamic losses during one cycle of input voltage (50Hz) are brought in Table II.

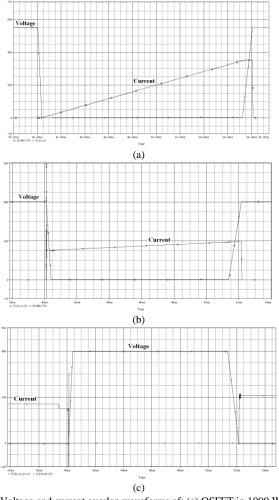


Fig. 4. Voltage and current overlap waveforms of: (a) OSFET in 1000 W output power and CCM operating mode. (b) MOSFET in 1000 W output power and DCM operating mode. (c) Output Diode in 1000 W output power and CCM operating mode.

Now, the failure rate of each part can be calculated as below:

A. Calculation of Failure Rate (λp) for MOSFET (APT8043BFLL):

$$V_n = 80V, \ \theta_{jc} = 0.31^{\circ} c / w, \ P_{loss} = 28.9 W$$
$$T_c = T_a + \theta_{ca} \times P_{loss} = 27 + 1 \times 28.9 = 55.9^{\circ}C$$

$$\begin{split} T_{j} &= T_{c} + \theta_{jc} \times P_{loss} = 55.9 + 0.31 \times 28.9 = 64.86 \,^{\circ}C \\ \pi_{T} &= \exp \Biggl(-1925 \times \Biggl(\frac{1}{T_{j} + 273} - \frac{1}{298} \Biggr) \Biggr) = 2.14 \\ \lambda_{b} &= 0.012, \, \pi_{E} = 6, \, P_{out} = 310W \Longrightarrow \pi_{A} = 10, \\ \pi_{O} &= 5.5 \end{split}$$

$$\lambda_{P} = \lambda_{b} \times \pi_{Q} \times \pi_{A} \times \pi_{E} \times \pi_{T}$$

= 0.012 \times 5.5 \times 10 \times 6 \times 2.14 = 8.48 (failure / 10⁶ h)

B. Calculation of Failure Rate for Output Diode (DSIE12):

$$V_{n} = 1200 V, \quad \theta_{jc} = 1.6^{\circ} C / w , P_{loss} = 12.4 W$$
$$T_{c} = T_{a} + \theta_{ca} \times P_{loss} = 27 + 1 \times 12.4 = 39.4^{\circ} C$$
$$T_{j} = T_{c} + \theta_{jc} \times P_{loss} = 39.4 + 1.6 \times 12.4 = 59.25^{\circ} C$$
$$\pi_{T} = \exp\left(-1925 \times \left(\frac{1}{T_{j} + 273} - \frac{1}{293}\right)\right) = 2.17$$
$$\lambda_{b} = 0.069, \quad \pi_{E} = 6, \quad \pi_{O} = 5.5, \quad \pi_{C} = 1$$

$$V_S = \frac{400}{1200} = 0.33 \Longrightarrow \pi_S = V_S^{2.43} = 0.07$$

 $\lambda_{P} = \lambda_{b} \times \pi_{E} \times \pi_{Q} \times \pi_{C} \times \pi_{S} \times \pi_{T}$ = 0.069 × 6 × 5.5 × 1 × 0.07 × 2.17 = 0.35 (failure / 10⁶ h)

C. Calculation of Failure Rate for Input Bridge:

$$V_{n} = 1200 V, \quad \theta_{jc} = 1.6^{\circ} C/w \quad P_{loss} = 8.4 W$$

$$T_{c} = T_{a} + \theta_{ca} \times P_{loss} = 27 + 1 \times 8.4 = 35.4^{\circ} C$$

$$T_{j} = T_{c} + \theta_{jc} \times P_{loss} = 35.4 + 1.6 \times 8.4 = 48.84^{\circ} C$$

$$\pi_{T} = \exp\left(-1925 \times \left(\frac{1}{T_{j} + 273} - \frac{1}{293}\right)\right) = 1.8$$

$$\lambda_{b} = 0.069, \; \pi_{E} = 6, \; \pi_{Q} = 5.5, \; \pi_{C} = 1$$

$$V_{s} = \frac{400}{1200} = 0.33 \Rightarrow \pi_{s} = V_{s}^{2.43} = 0.07$$

 $\lambda_P = \lambda_b \times \pi_E \times \pi_Q \times \pi_C \times \pi_S \times \pi_T$

 $= 0.069 \times 6 \times 5.5 \times 1 \times 0.07 \times 1.8 = 0.29$ (*failure* / 10⁶ *h*)

D. Calculation of Failure Rate for Inductor:

$$T_{HS} = T_a + 1.1 \times \Delta T = 27 + 1.1 \times 1.5 = 28.65 \,^{\circ}C$$
$$\lambda_b = 0.0016 \times \exp\left(\frac{T_{HS} + 273}{329}\right)^{15.6} = 2.2 \times 10^{-3}$$
$$\pi_O = 30, \ \pi_E = 6$$

 $\lambda_p = \lambda_b \times \pi_E \times \pi_O = 2.2 \times 10^{-3} \times 6 \times 30 = 0.39$ (faulure / 10⁶ h)

E. Calculation of Failure Rate (λ_P) for Capacitor:

$$\pi_{cv} = 0.34 \times C^{0.18} = 0.34 \times (680)^{0.18} = 1.1$$

$$S = \frac{400}{450} = 0.89, \quad T_A = 27^{\circ} C$$
$$\lambda_b = 0.00254 \left[\left(\frac{S}{0.5} \right)^3 + 1 \right] \exp \left[5.09 \times \left(\frac{T_A + 273}{358} \right)^5 \right]$$

$$\lambda_{b} = 0.14, \quad \pi_{Q} = 10, \quad \pi_{E} = 2, \quad \pi_{cr} = 1.1$$
$$\lambda_{p} = \lambda_{b} \times \pi_{Q} \times \pi_{E} \times \pi_{cv}$$
$$= 0.14 \times 10 \times 2 \times 1.1 = 3.08 \quad (failure/10^{6} h)$$

F. Failure Rate of Control Circuit Is Calculated in [4] and Is:

$$\lambda_{P(control \ circuit)} = 0.8858 \quad (failure / 10^6 h)$$

TABLE III: RESULTS OF RELIABILITY CALCULATIONS FOR DIFFERENT OUTPUT POWERS IN CCM/DCM OPERATING MODES

Control i control de la della della citta di la control di								
Output power	300W		600W		1000W			
Operating mode	CCM	DCM	CCM	DCM	CCM	DCM		
$\lambda_p(MOSFET)(failure/10^6 hours)$	7.04	11.24	9.07	14.18	8.48	14.32		
$\lambda_p(output Diode)(failure/10^6 hours)$	0.26	0.26	0.3	0.3	0.35	0.35		
$\lambda_p(input Bridge)(failure/10^6 hours)$	0.25	0.27	0.29	0.29	0.29	0.35		
$\lambda_p(output \ Capacitor)(failure/10^6 hours)$	2.52	2.96	2.88	2.97	3.08	3.25		
$\lambda_p(input Inductor)(failure/10^6 hours)$	0.37	0.37	0.38	0.38	0.39	0.39		
MTBF (hours)	95785	66225	77399	55187	79428	53590		

V. DISCUSSION OF RESULTS

Considering Table III, the following points can be concluded:

- The boost converter has higher reliability in CCM operating mode than in DCM operating mode.
- According to results, it can be seen that in all of output power ratings and both of DCM/CCM operating modes, switches has the highest failure rate in the converter structure. So, enough attention must be paid to them.
- Switches have higher failure rate in DCM mode than CCM modes. Since, in DCM mode, the peak and rms values of current is higher that results in higher current stress on switches in this mode. So, failure rate is higher in DCM mode.

VI. CONCLUSION

In this paper, a boost MPPT converter is simulated under three output power ratings and CCM/DCM operating modes. Then, reliability calculation of the converter is done based on MIL-HDBK-217 and in part counting method. Results have shown that switches have the highest failure rate in the converter structure in both CCM/DCM operating modes and different output powers. Since the peak and rms values of current are higher in DCM mode, then the switch sustains higher current stress in DCM mode, resulting in higher failure rate in this operating mode. So, the converter has better performance in CCM operating mode.

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